Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1488	((while during) adj3 (recharg$3 refresh$3)) near4 (dram ram sdram memory)	US-PGPUB; USPAT	OR	ON	2005/08/04 13:28
S2	1762874	(rout$3 re$1rout$3 enabl$3)	US-PGPUB; USPAT	OR	ON	2005/08/04 12:28
S3	21	S2 near4 S1	US-PGPUB; USPAT	OR	ON	2005/08/04 12:31
S4	3290	buffer near3 (coupl$3 latch$3) near4 (processor cpu controller)	US-PGPUB; USPAT	OR	ON	2005/08/04 12:32
S5	0	S1 same S4	US-PGPUB; USPAT	OR	ON	2005/08/04 12:32
S6	20	S1 and S4	US-PGPUB; USPAT	OR	ON	2005/08/04 12:32
S7	12	("3685020" | "3796996" | "3820078" | "4157587" | "4195340" | "4334287" | "4494190").PN. OR ("4737931").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/04 12:41
S8	12068	buffer with (mux multiplexer)	USPAT	OR	ON	2005/08/04 13:29
S9	3	S8 with S1	USPAT	OR	ON	2005/08/04 12:58
S10	39476	memory adj (control controller)	USPAT	OR	ON	2005/08/04 12:58
S11	317	S10 with S8	USPAT	OR	ON	2005/08/04 12:59
S12	36	S11 with (cpu processor)	USPAT	OR	ON	2005/08/04 13:07
S13	1909	(recharg$3 refresh$3) adj signal	USPAT	OR	ON	2005/08/04 13:07
S14	22	S13 near4 synchronized	USPAT	OR	ON	2005/08/04 13:08
S15	16	S14 and buffer	USPAT	OR	ON	2005/08/04 13:08
S16	44089	buffer with (select$3)	USPAT	OR	ON	2005/08/04 13:11
S17	10	S16 same S1	USPAT	OR	ON	2005/08/04 13:12
S18	15766	(recharg$3 refresh$3) near4 (dram ram sdram memory)	US-PGPUB; USPAT	OR	ON	2005/08/04 13:28
S19	15523	buffer with (mux multiplexer)	US-PGPUB; USPAT	OR	ON	2005/08/04 13:29
S20	113	S18 with S19	US-PGPUB; USPAT	OR	ON	2005/08/04 13:29
S21	10	S20 same (cpu processor)	US-PGPUB; USPAT	OR	ON	2005/08/04 13:30
S22	351090	input adj (data signal)	US-PGPUB; USPAT	OR	ON	2005/08/04 13:31
S23	443	(buffer adj receiv$3) near3 S22	US-PGPUB; USPAT	OR	ON	2005/08/04 13:31
S24	3	S23 adj4 (feed$3 fetch$3)	US-PGPUB; USPAT	OR	ON	2005/08/04 13:33
S25	5154	buffer adj4 (feed$3 fetch$3)	US-PGPUB; USPAT	OR	ON	2005/08/04 13:33
S26	144	S25 near4 (cpu processor)	US-PGPUB; USPAT	OR	ON	2005/08/04 13:33
S27	5	S26 with (while during)	US-PGPUB; USPAT	OR	ON	2005/08/04 13:33
S28	3190	secondary adj2 (memory catch)	USPAT	OR	ON	2005/08/04 15:16
S29	1488	((while during) adj3 (recharg$3 refresh$3)) near4 (dram ram sdram memory)	US-PGPUB; USPAT	OR	ON	2005/08/04 15:16
S30	0	S28 with S29	USPAT	OR	ON	2005/08/04 15:16
S31	0	S28 same S29	USPAT	OR	ON	2005/08/04 15:16
S32	16	S28 and S29	USPAT	OR	ON	2005/08/04 15:16

S33	12	("5442588" | "5483497" | "5818777" | "5835401" | "5999474" | "5999481" | "6185136" | "6233192" | "6282606").PN. OR ("6445636").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/04 15:19
S34	56153	(cpu processor) adj3 (read$3 access$3)	USPAT	OR	ON	2005/08/04 15:31
S35	19	S29 with S34	USPAT	OR	ON	2005/08/04 15:42
S36	74	(cpu processor) adj2 directly adj2 (receiv$3 accept$3)	USPAT	OR	ON	2005/08/04 15:43
S37	0	S36 same S29	USPAT	OR	ON	2005/08/04 15:43
S38	0	S36 and S29	USPAT	OR	ON	2005/08/04 15:43
S39	0	S36 with buffer	USPAT	OR	ON	2005/08/04 15:43
S40	31	S36 and buffer	USPAT	OR	ON	2005/08/04 15:43